



ATTORNEY DO NO. 041993-5151

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Seong-Kweon HA et al.

Application No.: 09/709,523

Filed: November 13, 2000

For: SEMICONDUCTOR PACKAGE AND
FABRICATION METHOD THEREOF

Group Art Unit: 2813

Examiner: D. Finsmith

Commissioner for Patents
Washington, D.C. 20231

Sir:

RESPONSE TO RESTRICTION REQUIREMENT

In response to the restriction requirement forwarded by the Office Action dated November 6, 2001, Applicants wish to elect Group I (claims 1-10), which the Examiner indicates as being drawn to a semiconductor package, without traverse. Accordingly, the Examiner is respectfully requested to examine elected claims 1 – 10 on their merits in this application. Applicants reserve the right to file a divisional application directed to the unelected claims.

Respectfully submitted,

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Dated: December 4, 2001

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